OP DETECTION

Advances in Counterexample Guided Abstraction-Refinement

Counterexample-guided abstraction-refinement based on predicate abstraction enables model checking large C programs (such as Windows device drivers). However, the technique is extremely inefficient on programs that contain deep loops. The first intermediate result of our research is a technique that solves this problem.





THE TRADITIONAL APPROACH A model checker exhaustively examines the abstract model and finds an abstract counterexample. The counterexample cannot be replayed on the original program and therefore the abstract model is refined, leading to another spurious counter-example. The abstract model is refined hundreds of times before the buffer overflow is finally detected.



States of the abstract model represent sets of states of the original program.

LOOP DETECTION choosing the short track

Instead of performing the tedious process of iterative unrolling we detect loops in a single step:





THE GORY DETAILS

In a post-processing step, we detect potential loops in the abstract counterexample, which consists of a sequence of abstract states (denoted by \hat{s}):

form $(i^{(N)}=i^{(o)}+N)$ and subsitute the corresponding occurrences in the loop body. Using a SAT-solver, we determine if there is a N that makes this parameterized execution trace feasible. If this is not the case, we proceed with the traditional abstraction-refinement algorithm. Otherwise, we simulate the unrolled counterexample. Feasible counterexamples constitute bugs and are reported to the user. Spurious counterexamples are used to refine the abstract model the usual way.



Potential loops are detected in the abstract counterexample

The simulation instance is parameterized in the number ofiterations

Using a SAT solver, we find the number of iterations that triggers the bug

FINDLOOPS $(\hat{s}_1, \ldots, \hat{s}_n)$ 1 foreach $i \in \{1, ..., n\}, j < i$: if $\exists \hat{s}'_j, \dots, \hat{s}'_i$. $\forall k \in \{j, \dots, i\} . \ell(\hat{s}'_k) = \ell(\hat{s}_k) \land$ $\forall k \in \{j, \dots, i-1\} : \hat{s}'_k \xrightarrow{a} \hat{s}'_{k+1} \land \hat{s}'_j = \hat{s}_j \land \hat{s}'_i \xrightarrow{a} \hat{s}'_j$ then insert $\|: \hat{s}'_i, \ldots, \hat{s}'_i :\|$ 5 **return** counterexample $\hat{s}_1, \ldots, \hat{s}_n$ with loops

The algorithm searches for transitions that can be taken to jump back to an abstract state that the trace has already WORK IN PROGRESS traversed. The counterexample is anno- With the support of Byron Cook, we have tated accordingly.

The annotated counterexample is map- nous abstract models into Microsoft's ped back to the original program. We abstraction-refinement toolkit SLAM. construct a recurrence equation for the Achieving scalability results comparable loop induction variable ($i^{(N)}=i^{(N-1)}+1$ in the to sequential analysis is still an on-going example from above), put it in its closed effort.

integrated a model checker for asynchro-

http://www.inf.ethz.ch/personal/daniekro/satabs/

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